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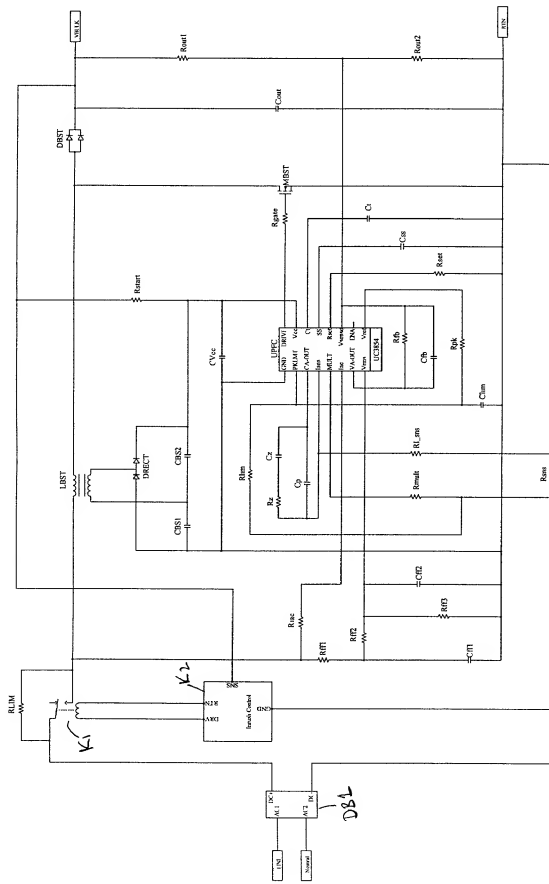
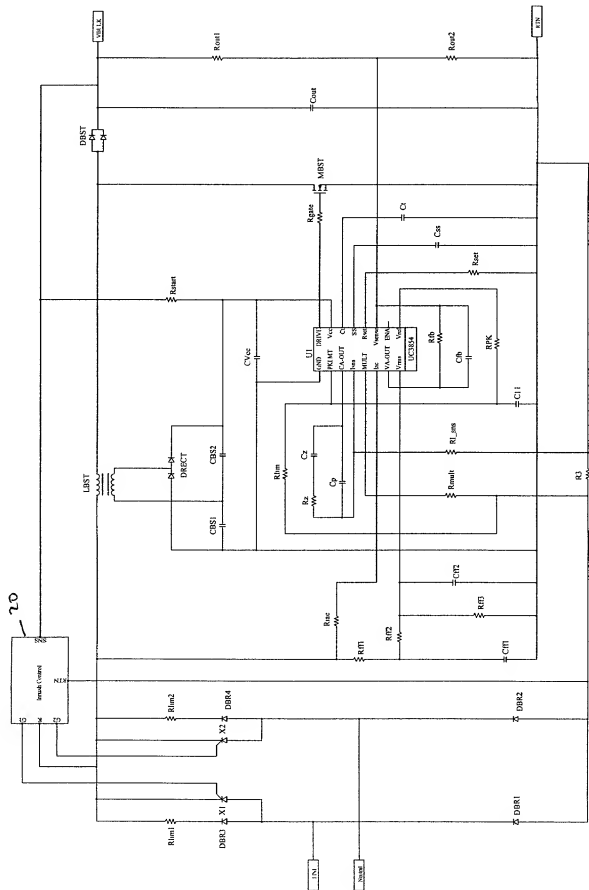


FIGURE 1 RELAY BASED INTRUSION CONTROL

PRIORITY



**FIGURE 2 SCR Based Inrush Control**

## Prior Art

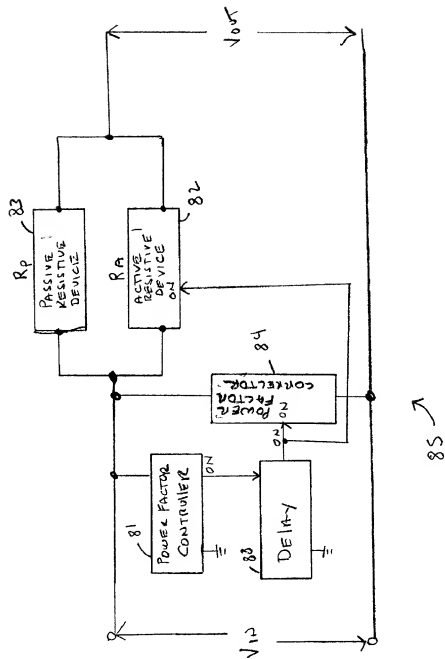


Fig. 3

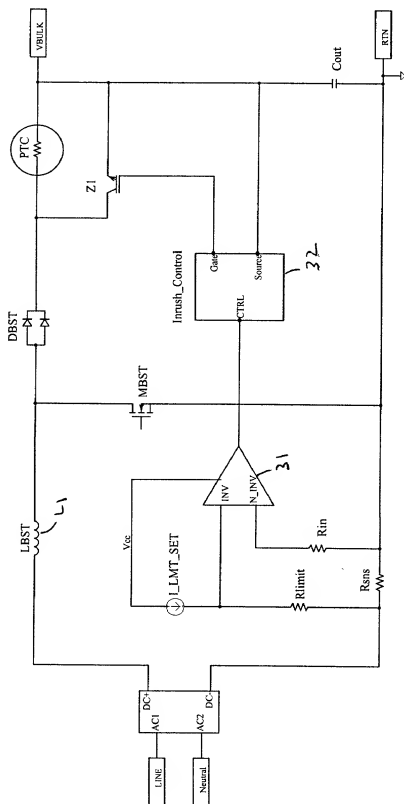


FIGURE 4 IGBT Based Inrush Control



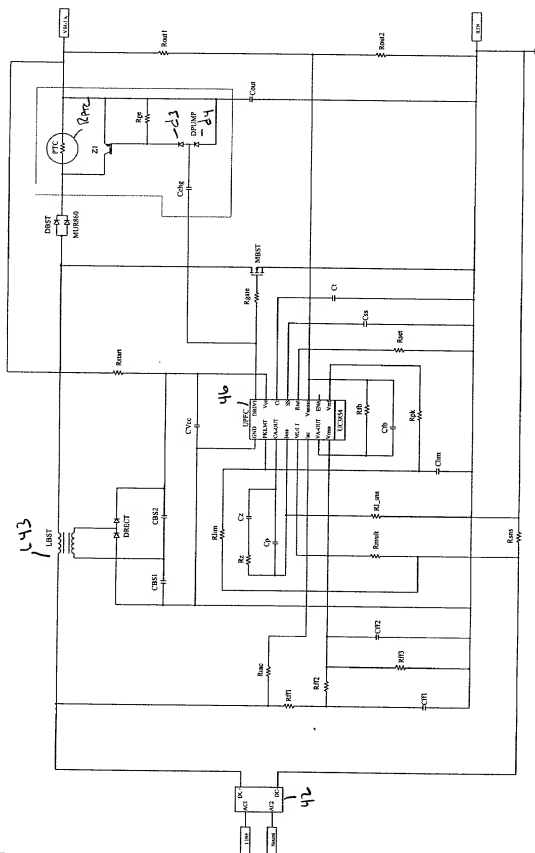


FIGURE 5 IGBT Based Inrush Control

Figure 6A-AC Input Line to Neutral Bridge

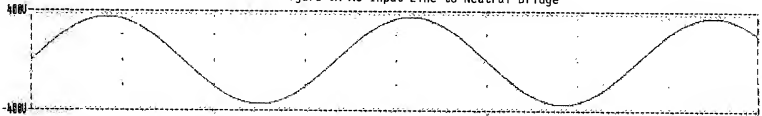


Figure 6B-Voltage Gate to Emitter of IGBT Z1

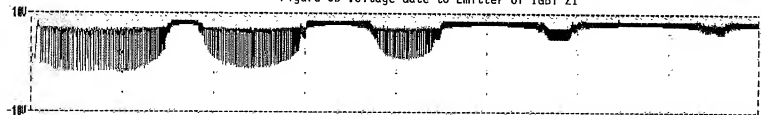


Figure 6C-Voltage Gate to Source of FET MBST



Figure 6D-Current through IGBT Z1 Collector

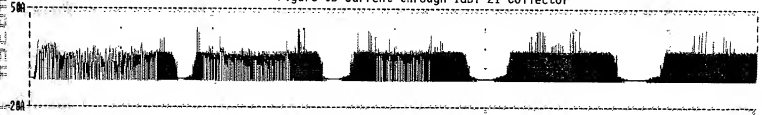


Figure 6E-Current through PTC Rptc

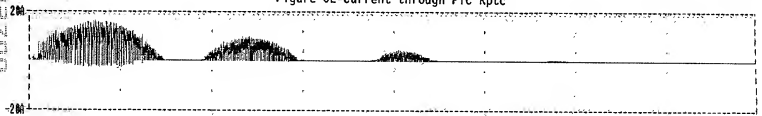


Figure 6F-Current into Line Input of Bridge

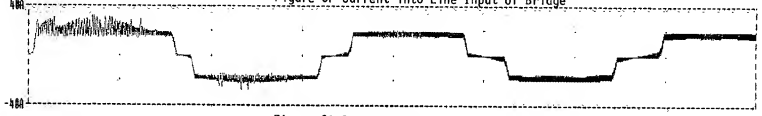
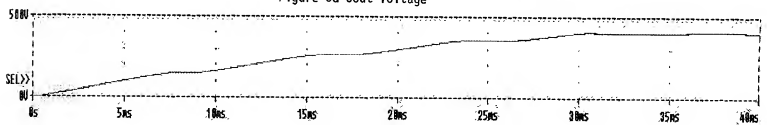


Figure 6G-Cout Voltage



Time

Figure 7

